	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	2907	(integrat\$4 with circuit) and modulat\$4 and coupl\$4 and (semiconductor with laser)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:27		
2	BRS	L2	190	1 and (extinction with ratio)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:28		
3	BRS	L3	0	1 and (extinction with ratio near5 controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:29		
4	BRS	L4	1	1 and (extinction with ratio with controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:31		
5	BRS	L5	81	extinction with ratio with controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:31		
6	BRS	L8	183542	6 and (semiconductor with laser)modulator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:32		
7	BRŚ	L9	1	6 and (semiconductor with laser)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:32		
8	BRS	L7	6	6 and modulator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:32		·
9	BRS	L6	11	5 and (integrated with circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:33		

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